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Docket No.: GR 97 P 6457 P

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By: J. P.

Date: April 22, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jörg Berthold et al.  
Applic. No. : 09/595,860 —  
Filed : June 16, 2000 —  
Title : Integrated Electrical Circuit and Method for Fabricating It  
Examiner : Cuong Q. Nguyen - Art Unit: 2811

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. 1.97(C)(1)

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Korean Patent Application KR 1997-0053559, dated July 31, 1997, and English abstract thereof;

Korean Office Action dated February 25, 2003.

In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

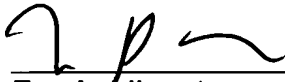
If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

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patents and/or publications, it is because no existing translation is readily available to the applicant.

In accordance with 37 C.F.R. 1.97 (c) (1), consideration of this Information Disclosure Statement is requested.

Respectfully submitted,

  
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For Applicants

*Mark P. Weichselbaum  
Reg. No. 43,248*

Date: April 22, 2003

Lerner And Greenberg, P.A.  
Post Office Box 2480  
Hollywood, FL 33022-2480  
Tel: (954) 925-1100  
Fax: (954) 925-1101

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